

L Number	Hits	Search Text	DB	Time stamp
1	304	first adj status and second adj status and third adj status	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:01
2	223640	controller and ((read or write) or (memory near3 store))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:01
3	86	(first adj status and second adj status and third adj status) and (controller and ((read or write) or (memory near3 store)))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:01
4	107524	archive or archived or backup or back-up	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:01
5	7	((first adj status and second adj status and third adj status) and (controller and ((read or write) or (memory near3 store)))) and (archive or archived or backup or back-up)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:01
6	2390	memory adj board	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:02
7	22	first adj memory adj board and second adj memory adj board	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:02
8	0	(host adj controller or i/o adj controller) and (first adj memory adj board and second adj memory adj board)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:02
9	4	(archive or archived or backup or back-up) and (first adj memory adj board and second adj memory adj board)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:02
10	0	(first adj status and second adj status and third adj status) and (first adj memory adj board and second adj memory adj board)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:02
11	0	(first adj status and second adj status and third adj status) and ((archive or archived or backup or back-up) and (first adj memory adj board and second adj memory adj board))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:02
12	0	(host adj controller or i/o adj controller) and ((first adj status and second adj status and third adj status) and (archive or archived or backup or back-up))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:02
13	0	(first adj memory adj board and second adj memory adj board) and ((first adj status and second adj status and third adj status) and (archive or archived or backup or back-up))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:03
14	26	(first adj status and second adj status and third adj status) and (archive or archived or backup or back-up)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:03
15	1596	backup with status	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:03
16	0	(first adj memory adj board and second adj memory adj board) and (backup with status)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:03
17	316	archive with status	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:03

18	0	(first adj memory adj board and second adj memory adj board) and (archive with status)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:03
19	528	((controller and ((read or write) or (memory near3 store))) and (backup with status))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:03
20	528	(archive or archived or backup or back-up) and ((controller and ((read or write) or (memory near3 store))) and (backup with status))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:03
21	0	(first adj memory adj board and second adj memory adj board) and ((archive or archived or backup or back-up) and ((controller and ((read or write) or (memory near3 store))) and (backup with status)))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:04
22	3	(first adj status and second adj status and third adj status) and (backup with status)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:04
23	62	second adj copy with (status or valid)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:04
24	0	(first adj status and second adj status and third adj status) and (second adj copy with (status or valid))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:04
25	0	(first adj memory adj board and second adj memory adj board) and (second adj copy with (status or valid))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:04
26	0	(backup with status) and (second adj copy with (status or valid))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:05
27	133	(host adj controller or i/o adj controller) and (memory adj board)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:05
28	0	((host adj controller or i/o adj controller) and (memory adj board)) and (first adj status and second adj status and third adj status)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:05
29	0	((host adj controller or i/o adj controller) and (memory adj board)) and (second adj copy with (status or valid))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:05
30	799	711/162.ccls.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:05
31	643	711/156.ccls.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:05
32	1179	711/167.ccls.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:05
33	1142	711/112.ccls.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:05
34	1281	711/114.ccls.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:06

35	15	(dual adj write or dual-write) with (cache or cached or caching)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:06
36	277	711/130.ccls.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:06
37	532	711/151.ccls.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/25 17:06